



Figure 1 Dual channel receiver flow chart.

The diagram illustrates the system architecture. On the left, the **SOURCE DEVICE** receives **BLOCKS ARRIVE FROM NETWORK** into a **QUEUE**. These blocks pass through a **DUAL CHANNEL SEQUENCER** to two transmitters: an **EVEN TRANSMITTER** and an **ODD TRANSMITTER**. The **EVEN TRANSMITTER** sends data to an **EVEN RECEIVER** in the **DESTINATION DEVICE**, while the **ODD TRANSMITTER** sends data to an **ODD RECEIVER**. A **DATA CHANNEL**, represented by a hatched box, receives input from both transmitters and provides output to both receivers. Four control/feedback channels connect the devices: **EVEN FEEDBACK CHANNEL** (Even Receiver to Even Transmitter), **EVEN CONTROL CHANNEL** (Even Transmitter to Even Receiver), **ODD CONTROL CHANNEL** (Odd Transmitter to Odd Receiver), and **ODD FEEDBACK CHANNEL** (Odd Receiver to Odd Transmitter). Finally, both the **EVEN HYBRID ARQ DECODER** and **ODD HYBRID ARQ DECODER** in the destination device output **PACKETS RELEASED**.

